

## ABSTRACT:

The video signal enhancement unit (100) and the image display apparatus containing the unit have a processing unit (102) for contrast modification of the video signal. The processing unit (102) receives input data from a contrast counter (112), designed to store a contrast count. The contrast count is decreased by a pixel counter (104) each time the total 5 number of pixels, that occur within a predetermined period of time and which have video signal levels that are higher than a predetermined video level, exceeds a predefined threshold. This predetermined period of time is a number of times shorter than one video field interval. The contrast count is increased each time a trigger means (114) generates a pulse. A contrast 10 comparator (116) can be coupled to the contrast counter (112) to limit the contrast count to a maximum contrast value.

Fig. 1

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